



10th Joint International EuroSOI Workshop and International Conference on Ultimate Integration on Silicon (EuroSOI-ULIS) 2024

15th - 17th May 2024 Athens (Greece)

The [Conference](#) aims at gathering together scientists and engineers working in academia, research centres and industry in the field of Ultra Large Scaling Integration nanoscale devices in More-Moore and More-Than-Moore scenarios implemented on bulk Si and SOI technologies.

High quality contributions in the following areas are solicited:

1. Advanced SOI materials and structures, innovative SOI devices.
2. Alternative transistor architectures (FDSOI, Nanowire, Nanosheet, FinFET, MuGFET, vertical MOSFET, FeFET and TFET, MEMS/NEMS, Beyond-CMOS).
3. New channel materials for CMOS (strained Si/Ge, III-V, carbon nanotubes; graphene and other 2D materials).
4. Properties of ultra-thin semiconductor films and buried oxides, defects, interface quality; thin gate dielectrics: high- κ and ferroelectric materials for switches and memory.
5. New functionalities and innovative devices in More than Moore domains such as nanoelectronic sensors, biosensors, energy harvesting devices, CMOS imagers, photonic integrated circuits, etc.
6. Transport phenomena, compact modeling, device simulation, front- and back-end process simulation.
7. CMOS scaling perspectives; device/circuit level performance evaluation; RF devices and switches; three-dimensional integration of devices and circuits, heterogeneous integration.
8. Advanced test structures and characterization techniques, parameter extraction, reliability and variability assessment techniques for new materials and novel devices.
9. Memory devices, memristors, hardware for unconventional computing such as neuromorphic, in-memory, in-sensor and quantum computing.

PUBLICATION

Original *2-page abstracts* with illustrations will be reviewed by the Scientific Committee. The accepted contributions will be published as *4-page letters* in a special issue of the Elsevier journal Solid-State Electronics. Extended versions of outstanding papers will be published in a further special issue of Solid-State Electronics.



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AWARDS

The “**Androula Nassiopoulou Best Paper Award**” will be attributed by the SINANO Institute.

A “**Best Poster Award**” will be attributed by ELSEVIER.

SATELLITE EVENTS

In addition, an important [Satellite event](#) will be organized:

- ◆ The **ICOS** “International Cooperation on Semiconductors” Workshop will take place on May 13-14, 2024 at the same location - free registration

Important dates:

Abstract submission deadline: **March 1, 2024**

Notification of acceptance: **March 15, 2024**

Registration and abstract submission [here](#).

For more information you can visit the conference website [here](#)

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